



## **U.S. UTILITY Patent Application**

	<b>O.I.P.E.</b>	<b>PATENT DATE</b>
<b>SCANNED</b>	<b>NS4</b>	<b>QA</b>
		

APPLICATION NO. 09/649437	CONT/PRIOR:	CLASS <del>203</del> #11	SUBCLASS <del>14</del>	ART UNIT 2123	EXAMINER <del>J. A. S.</del>
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ASPI

Method and apparatus for specifying addressability and bus connections in a logic design C

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PTO-2040  
12/09

## **ISSUING CLASSIFICATION**

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date) _____				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
	(Primary Examiner) _____ (Date) _____			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner) _____ (Date) _____				

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